



JOHND

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10056770	FILING DATE 01/24/2002	CLASS 219	SUBCLASS T03	GAU 1752	EXAMINER 2878 LONG TRAN
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**APPLICANTS: Ferber Gottfried; Pelmer Reimund;

**CONTINUING DATA VERIFIED: N UKT

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** FOREIGN APPLICATIONS VERIFIED:
GERMANY 101 03 084.3 01/24/2001 Y UKT

PG-PUB [DO NOT PUBLISH] <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials UKT	ATTORNEY DOCKET NO WMP-EUP-008
TITLE : Semiconductor module and method for fabricating the semiconductor module U.S. DEPT. OF COMM / PAT & TM-PTO-436L (Rev. 12-94)	

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheeta Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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